



<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (use as many sheets as necessary)			<b>Complete if Known</b>		
			Application Number	10/757,939	
			Filing Date	January 16, 2004	
			First Named Inventor	Craig HANSEN	
			Group Art Unit	2183	
			Examiner Name	CHAN, EDDIE P.	
Sheet	1	of	4	Attorney Docket Number	43876-153

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code <sup>2</sup> (if known)			
		US-5,327,369	07-05-1994	Ashkenazi	
		US-5,409,469	04-1995	Chung	
		US-5,448,509	09-05-1995	Lee	
		US-5,467,131	11-14-1995	Bhaskaran	
		US-5,541,865	07-30-1996	Ashkenazi	
		US-5,742,782	04-1998	Ito	
		US-5,883,824	03-16-1999	Lee	
		US-5,887,183	03-1999	Agarwal	

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
		Country Code <sup>3</sup> Number 4- Kind Code <sup>5</sup> (if known)				

Examiner Signature		Date Considered	
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Sheet	2	of	4	Attorney Docket Number	43876-153

OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate) title of the item (book, magazine, journal, serial, symposium, catalog, etc), date, page(s), volume-issued number(s), publisher, city and/or country where published.	T <sup>2</sup>
		<b>MU v. SCEA</b>	
		5/1/07 Exhibits A-2, E-2, and F-2 to SCEA's Supplemental Invalidity Contentions filed 5/1/07 ( <i>previously marked Outside Counsel Eyes Only, now released for submission</i> ) ( <i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> ; 2:05-cv-505; USDC for the Eastern District of Texas, Marshall Division)	
		7/11/07 SCEA's Second Amended Answer, Affirmative Defenses, And Counterclaims to MicroUnity's First Amended Complaint ( <i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> , 2:05-cv-505, U.S.D.C., E.D. Texas, Marshall Division)	
		7/11/07 P.R. 4-3 Joint Claim Construction Statement ( <i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> , 2:05-cv-505, U.S.D.C., E.D. Texas, Marshall Division)	
		7/11/07 P.R. 4-3 Joint Claim Construction Statement – MU's Supporting Evidence – Reference #2: Definitions of "execution" and "general purpose computer", The IEEE Standard Dictionary of Electrical and Electronics Terms, 6 <sup>th</sup> ed. (IEEE 1996), pp. 379, 451 and 1232 ( <i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> , 2:05-cv-505, U.S.D.C., E.D. Texas, Marshall Division)	
		7/11/07 P.R. 4-3 Joint Claim Construction Statement – MU's Supporting Evidence – Reference #3: Definition of "execution", Modern Dictionary of Electronics, 6th ed. revised and updated (Newnes/Butterworth-Heinemann 1997), p. 355 ( <i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> , 2:05-cv-505, U.S.D.C., E.D. Texas, Marshall Division)	
		7/11/07 P.R. 4-3 Joint Claim Construction Statement – MU's Supporting Evidence – Reference #5: 3/9/05 Joint Claim Construction and Prehearing Statement in <i>MicroUnity Engineering Systems, Inc. v. Dell, Inc. and Intel Corporation</i> , No. 2-04-CV-120 (U.S.D.C., E.D. Texas) ( <i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> , 2:05-cv-505, U.S.D.C., E.D. Texas, Marshall Division)	
		8/24/07 MU's LRP 4-5(a) Opening Brief on Claim Construction, and Exhibits 1-14 ( <i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> , 2:05-cv-505, U.S.D.C., E.D. Texas, Marshall Division)	
		9/12/07 SCEA's Responsive Brief Regarding Claim Construction Pursuant to P.R. 4-5(b), and Exhibits 1-34 ( <i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> , 2:05-cv-505, U.S.D.C., E.D. Texas, Marshall Division)	
		9/17/07 LPR 4-5(c) Reply Brief on Claim Construction from <i>MicroUnity Systems Engineering, Inc.</i> , and Exhibits 15 and 16 ( <i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> , 2:05-cv-505, U.S.D.C., E.D. Texas, Marshall Division)	
		10/15/07 SCEA's Motion for Partial Summary Judgment of Invalidity for U.S. Patent Nos. 6,643,765 and 6,725,356, and Proposed Order, and Exhibits A-U ( <i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> , 2:05-cv-505, U.S.D.C., E.D. Texas, Marshall Division)	
		10/29/07 MU's Response Brief in Opposition to Sony's Motion for Partial Summary Judgment of Invalidity of U.S. Patent Nos. 6,643,765 and 6,725,356, and Proposed Order ( <i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> , 2:05-cv-505, U.S.D.C., E.D. Texas, Marshall Division)	

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		BIT Data Sheet - Product Summary: B3110/B3120/B2110/B2120 Floating Point Chip Set, Bipolar Integrated Technology, Inc. (Dec. 1986)	
		Colwell et al., "Architecture and Implementation of a VLIW Supercomputer," IEEE, 910-19 (1990)	
		Colwell et al., "A VLIW Architecture for a Trace Scheduling Compiler," IEEE Transactions on Computers, vol.37, no.8, 967-79 (Aug. 1988)	
		Hwang et al., Computer Architecture and Parallel Processing, McGraw Hill Book Co., Singapore (1988) (7 pages)	
		IEEE Standard for Scalable Coherent Interface (SCI), IEEE Std 1596-1992, © (1993)	
		Lowney et al., "The Multiflow Trace Scheduling Compiler," (Oct. 30, 1992)	
		"MIPS Digital Media Extension" (MDMX), rev. 1.0, C-1 to C-40	
		"Multimedia Extension Unit for the X86 Architecture," Compaq Computer Corp., Revision 0.8b (Jun. 20, 1995)	
		"PA-RISC 2.0 Architecture and Instruction Set Reference Manual," Hewlett-Packard Co. (1995)	
		"TRACE /300 Series: F Board Architecture," Multiflow Computer (Dec. 9, 1988)	
		X86 64-Bit Extension Multimedia Instruction Set Architecture, Intel 64-bit Multimedia ISA Ratification Summit (Apr.-May 1992) [SCEA-1423558-655] ( <i>Previously marked Confidential - Counsel Eyes Only, but released for submission</i> )	
		Office Action dated May 1, 2006 with Notice of References Cited by Examiner received in related Patent Application No. 10/757,515	
		Amendment filed November 1, 2006 in response to Office Action dated May 1, 2006 in related Patent Application No. 10/757,515	
		Office Action dated January 29, 2007 received in related Patent Application No. 10/757,515	
		Amendment and Declaration of Korbin Van Dyke filed May 29, 2007 in response to Office Action dated January 29, 2007 in related Patent Application No. 10/757,515	
		Office Action dated August 10, 2007 received in related Patent Application No. 10/757,515	
		Amendment filed September 10, 2007 in response to Office Action dated August 10, 2007 in related Patent Application No. 10/757,515	
		Notice of Allowance and Fee(s) Due dated November 28, 2007 received in related Patent Application No. 10/757,515	

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